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The invisible watermarking

algorithm used here allows

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identity of the carrier. In this paper, we present an architecture and a hardware efficient FPGA based invisible watermark module towards the development of the complete digital camera.

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... FPGA-based
implementations of MD5,
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cryptographic functions have
exploited this sort of bit-
level operation. Even public-
key cryptographic systems
have been built atop FPGAs.
Similarly, there are various
FPGA-based intrusion-
detection systems (IDS). All
this work centers around
exploiting FPGAs Managing
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FPGA BASED IMAGE SECURITY
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DIGITAL CAMERA USING
INVISIBLE WATERMARKING
TECHNIQUE . By A. MOHAMED
ZUHAIR and C. MOHAMED
YOUSUF. Abstract. For
effective digital rights
management (DRM) of
multimedia in the framework
of embedded systems, both
watermarking and
cryptography are necessary.
Here, we present a novel
system in the ...

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Fpga Based Image Security And The invisible watermarking algorithm used here allows for verification of the image as well as the identity of the carrier. In this paper, we present an architecture and a hardware efficient FPGA based invisible watermark module towards the development of the complete digital camera.

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FPGA BASED IMAGE SECURITY AND AUTHENTICATION IN DIGITAL ... Incorporation of encryption and watermarking together in the digital camera will assist in protecting and authenticating image files. In this paper, we present an architecture and a hardware... (PDF) VLSI architecture and FPGA prototyping of a digital ... IMAGING & VISION Gidel has been a technology leader in high performance,

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You acquire the image using

the CPU and then send it to

the FPGA via direct memory

access (DMA) so the FPGA can

perform operations such as

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filtering or color plane extraction. Then you can send the image back to the CPU for more advanced operations such as optical character recognition (OCR) or pattern matching.

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Intel FPGAs offer both

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volatile and non-volatile key storage. The key is stored in FPGAs when using the design security feature. Depending on the security mode, you can configure the FPGAs with a configuration file that is encrypted with the same key, or for board testing, configure with a normal configuration file.

~~AN 556: Using the Design Security Features in Intel FPGAs~~

FPGA-based implementations of MD5, SHA-2, and various other cryptographic functions have exploited this sort of bit-level operation. Even public-key cryptographic systems have

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been built atop FPGAs. Similarly, there are various FPGA-based intrusion-detection systems (IDS). All this work centers around exploiting FPGAs

~~Managing Security in FPGA-Based Embedded Systems~~

A field-programmable gate array (FPGA) is an integrated circuit designed to be configured by a customer or a designer after manufacturing - hence the term "field-programmable". The FPGA configuration is generally specified using a hardware description language (HDL), similar to that used for an application-specific

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integrated circuit (ASIC).

Circuit diagrams were previously used to specify ...

~~Field programmable gate array — Wikipedia~~

This is another great application for FPGA based image processing, the ability to work with the sensors for long integration times and process and store the image is a real benefit. Of course in a real system, you might even stop the majority of the FPGA clocks while the image is being captured to prevent the additional noise from being injected into the captured image.

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Image: ZDNet, Priyanka from the Noun Project In a new research paper published on the last day of 2019, a team of American and German academics has shown that field-programmable gate array (FPGA)...

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